

Amendments to the Claims

1. (CURRENTLY AMENDED) A method of protecting an integrated circuit against damage due to excessive temperatures, the integrated circuit comprising two or more transistors, ~~(208)~~the method comprising the steps of:

- i) providing temperature measurement means ~~(200)~~in or on each of said transistors~~(208)~~; and
 - ii) if the measured temperature in any of said transistors ~~(208)~~exceeds a first predetermined threshold temperature, disabling at least one of said transistors ~~(208)~~ for a predetermined period of time;
- the method being characterized in that the temperature measurement means ~~(200)~~ comprises means for measuring current through a reverse biased diode ~~(10)~~provided in respect of each of said transistors~~(208)~~.

2. (CURRENTLY AMENDED) A method according to claim 1, wherein said transistors ~~(208)~~are power transistors.

3. (CURRENTLY AMENDED) A method according to ~~claim 1 or claim 2~~claim 1, wherein said reverse biased diode ~~(10)~~is comprises a reverse biased pn junction in said integrated circuit.

4. (CURRENTLY AMENDED) A method according to ~~any one of claims 1 to 3~~claim 1, including the step of providing temperature measurement means ~~(210)~~in respect of the small signal part of the integrated circuit, and if the measured temperature of said small signal part of the integrated circuit exceeds a second predetermined threshold temperature, disabling said transistors ~~(208)~~for a predetermined period of time.

5. (CURRENTLY AMENDED) A method according to claim 4, wherein said temperature measurement means ~~(210)~~provided in respect of said small signal part of the integrated circuit comprises means for measuring current through a reverse biased diode.

6. (ORIGINAL) A method according to claim 4, wherein said first predetermined threshold temperature is greater than said second predetermined threshold temperature.

7. (CURRENTLY AMENDED) A method according to ~~any one of claims 1 to 6~~claim 1, wherein said first and second predetermined threshold temperatures are in the range 130° – 200°C.

8. (CURRENTLY AMENDED) A method according to ~~any one of claims 1 to 7~~claim 1, wherein said predetermined period of time is in the range 2 to 5 ms, and the method includes the step of automatically enabling the transistors ~~(208)~~ when said predetermined period of time has elapsed.

9. (CURRENTLY AMENDED) Apparatus for protecting an integrated circuit against damage due to excessive temperatures, the integrated circuit comprising two or more transistors ~~(208)~~, the apparatus comprising:

- i) means ~~(200)~~ for measuring temperature in each of said transistors ~~(208)~~; and
- ii) means ~~(204,206)~~ for disabling at least one of said transistors ~~(208)~~ for a predetermined period of time if the measured temperature in any of said transistors ~~(208)~~ exceeds a predetermined threshold temperature;

the apparatus being characterized in that the means ~~(200)~~ for measuring temperature comprises means for measuring current through a reverse biased diode ~~(10)~~ provided in respect of each of said transistors ~~(208)~~.

10. (CURRENTLY AMENDED) Apparatus according to claim 9, including means ~~(210)~~ for measuring temperature in the small signal part of the integrated circuit.

11. (CURRENTLY AMENDED) Apparatus according to claim 10, wherein said means ~~(210)~~ for measuring temperature in the small signal part of the integrated circuit comprises means for measuring current through reverse biased diode.

12. (CURRENTLY AMENDED) Apparatus according to ~~any one of claims 9 to 11~~claim 9, wherein said transistors ~~(208)~~ are power transistors.

13. (CURRENTLY AMENDED) Apparatus according to ~~any one of claims 9 to 12~~claim 9, comprising shut-off means ~~(206)~~ for disabling all of said transistors in the event that the temperature in any of said transistors is determined to exceed said predetermined threshold temperature.

14. (CURRENTLY AMENDED) Apparatus according to claim 13, comprising timer means ~~(204)~~ for causing said transistors ~~(208)~~ to be enabled when said predetermined period of time has elapsed.

15. (CURRENTLY AMENDED) Apparatus according to ~~any one of claims 9 to 14~~claim 9, wherein said reverse biased diode ~~(10)~~ comprises a reverse biased pn junction provided in said integrated circuit.

16. (CURRENTLY AMENDED) An integrated circuit comprising a plurality of power transistors ~~(208)~~, and apparatus for protecting said integrated circuit against damage due to excessive temperatures according to ~~any one of claims 9 to 15~~claim 9, said apparatus comprising means ~~(200)~~ for measuring temperature in each of said transistors ~~(208)~~ and a common shut-off ~~(206)~~ and timer means ~~(204)~~ for disabling all of said transistors ~~(208)~~ in the event that the temperature in any one of said transistors ~~(208)~~ is determined to exceed a predetermined threshold temperature and re-enabling said transistors when said predetermined period of time has elapsed.

17. (ORIGINAL) An amplifier including an integrated circuit according to claim 16.

18. (CURRENTLY AMENDED) A method of manufacturing an integrated circuit comprising two or more transistors ~~(208)~~, the method including the steps of providing, in respect of each of said transistors ~~(208)~~, a reverse biased diode ~~(10)~~, means for monitoring current through each of said diodes ~~(10)~~ to determine the temperature of respective transistors ~~(208)~~, and providing means ~~(204,206)~~ for disabling, for a

predetermined period of time, one or more of said transistors ~~(208)~~ in the event that the temperature of any of said transistors ~~(208)~~ is determined to exceed a predetermined threshold temperature.